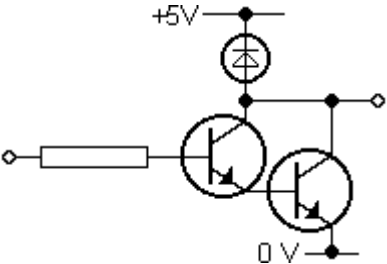
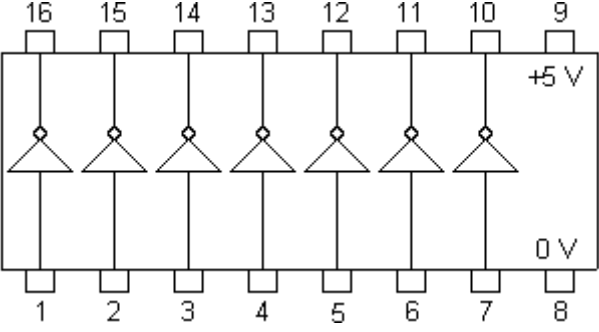
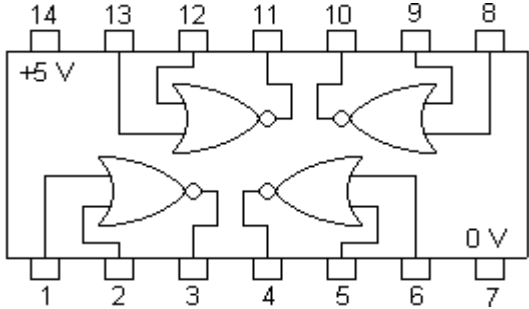
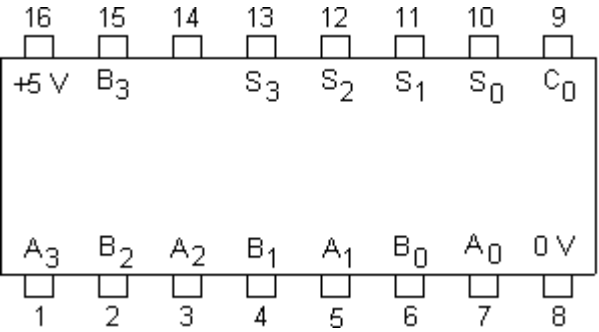
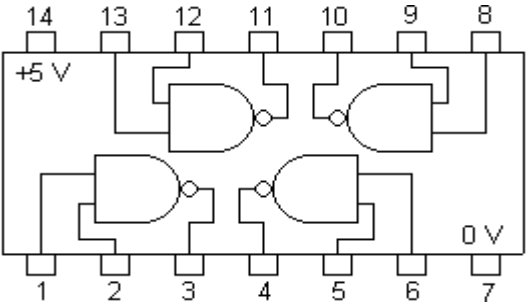
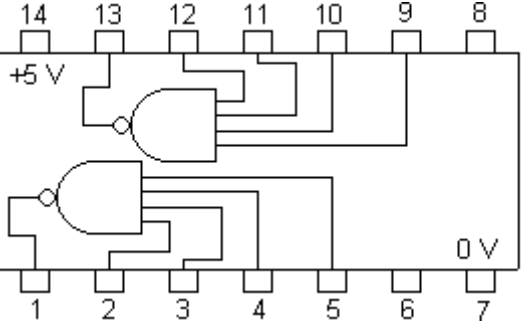
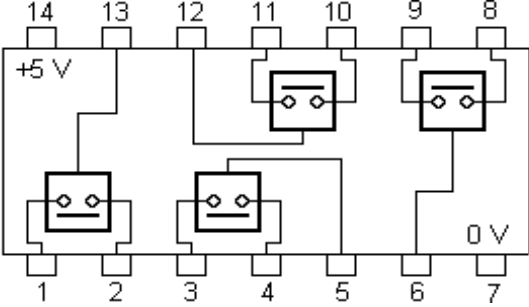
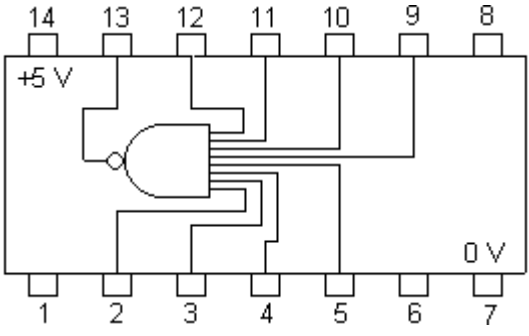
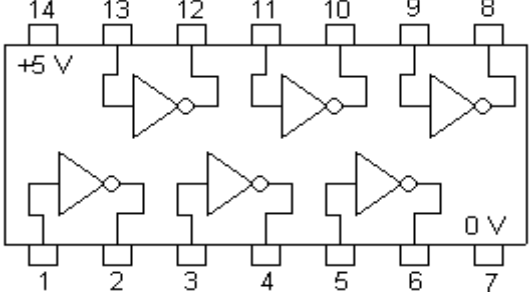
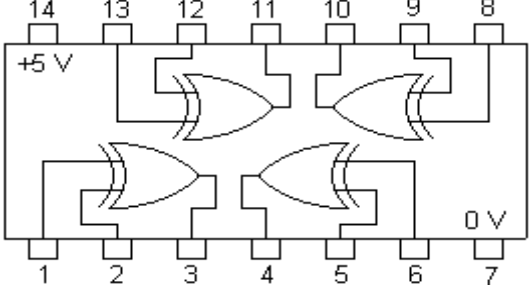
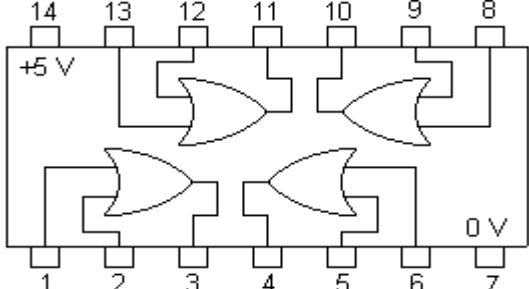


<p><b>Digital integrated circuits</b></p> <p><b>2004 drivers</b></p> 	
<p><b>4001 dual input NOR</b></p> $Q = \overline{A + B}$	
<p><b>4008 four-bit adder</b></p> $S_3 S_2 S_1 S_0 = A_3 A_2 A_1 A_0 + B_3 B_2 B_1 B_0 + C_0$	
<p><b>4011 two-input NAND</b></p> $Q = \overline{A \cdot B}$	
<p><b>4012 four-input NAND</b></p> $Q = \overline{A \cdot B \cdot C \cdot D}$	

<p><b>4013 dual D-type flip-flop</b>  <math>Q = D, \bar{Q} = \bar{D}</math> when CK rises  <math>Q = 1, \bar{Q} = 0</math> if <math>S = 1</math>  <math>Q = 0, \bar{Q} = 1</math> if <math>R = 1</math></p>	
<p><b>4024 seven bit counter</b>  GFEDCBA = 0000000 when R = 1  counts rising edges at CK</p>	
<p><b>4035 shift register</b>  join the SI inputs together  DCBA = dcba when <math>P/\bar{S}</math> is high  and CK rises from 0 to 1  When <math>P/\bar{S}</math> is low, <math>D \rightarrow C</math> etc.  on rising edges at CK</p>	
<p><b>4051 eight-bit multiplexer</b>  <math>Q = I_n</math> where <math>n = 4C + 2B + A</math></p>	
<p><b>4052 dual four-bit multiplexer</b>  <math>Q = I_n</math> where <math>n = 2B + A</math></p>	

<p><b>4066 quad two-input multiplexer</b> switch closes when control is high</p>	
<p><b>4068 eight-input NAND</b> <math>Q = \overline{A.B.C.D.E.F.G.H}</math></p>	
<p><b>4069 hex inverters</b> <math>Q = \overline{A}</math></p>	
<p><b>4070 Exclusive-OR gates</b> <math>Q = \overline{A}.B + A.\overline{B}</math></p>	
<p><b>4071 two-input OR gates</b> <math>Q = A + B</math></p>	

<p><b>4081 two-input AND gates</b></p> <p><math>Q = A \cdot B</math></p>	
<p><b>4511 BCD-to-LED decoder</b></p> <p>g fedcba frozen when <math>\overline{ST} = 1</math>  g fedcba = 1 when <math>\overline{LT} = 0</math>  g fedcba = 0 when <math>\overline{BL} = 0</math></p> <p>f            b  g            c            g fedcba <math>\equiv</math> DCBA  e            d</p>	
<p><b>40106 Schmitt triggers</b></p> <p><math>Q = 1</math> when <math>A \leq 2.2V</math>  <math>Q = 0</math> when <math>A \geq 2.8V</math></p>	
<p><b>6116 RAM</b></p>	